

PATENT (5500-68500/TT4418)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

10/061, 671

Filed: February 1, 2002

Inventor(s):

Michael A. Filippo

Title:

Apparatus and Method for Selective Clock Control to

Reduce Power Consumption

in an Integrated Circuit

Examiner:

Wang, Albert C

Group/Art Unit:

2115

Atty. Dkt. No:

5500-68500

Customer No.

31266

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, M/S Issue Fee, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Name of Registered Representative

5/

Date

NOTICE OF CHANGE OF FEE ADDRESS

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M/S ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 RECEIVED OIPE/IAP

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Sir:

Applicant respectfully requests the Commissioner to change the correspondence address for **Maintenance Fees** with regard to the above identified patent application. The **Fee** Address is:

AMD
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Respectfully submitted,

Robert C. Kowert Reg. No. 39,255

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